



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov



CONFIRMATION NO. 1091

Bib Data Sheet

SERIAL NUMBER 10/773,276	FILING DATE 02/09/2004 RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. 2004_0156A
-----------------------------	-----------------------------------	--------------	------------------------	-----------------------------------

## APPLICANTS

Tomoo Kimura, Fukuoka, JAPAN;

Kenichi Ishida, Kyotanabe, JAPAN;  
Tomoyuki Inomoto, Iizuka, JAPAN;\*\* CONTINUING DATA *N* *VS* \*\*\*\*\*\*\* FOREIGN APPLICATIONS *VS* \*\*\*\*\*

JAPAN 2003-032438 02/10/2003 NO

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 06/14/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	<i>WENDEROTH</i> <i>VS</i> Examiner's Signature <i>VS</i> Initials	JAPAN	DRAWING 17	CLAIMS 22	CLAIMS 6

## ADDRESS

000513  
 WENDEROTH, LIND & PONACK, L.L.P.  
 2033 K STREET N. W.  
 SUITE 800  
 WASHINGTON , DC  
 20006-1021

## TITLE

Logic circuit optimizing method, logic circuit optimizing device and logic circuit composing device

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
RECEIVED 1194		